PATENT ABSTRACTS OF JAPAN

(11)Publication number: 2002-342165 (43)Date of publication of application: 29.11.2002

(51)Int.Cl. G06F 12/14 G06F 15/16

(21)Application number: 2002-079358 (71)Applicant: SONY COMPUTER ENTERTAINMENT INC

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(30)Priority

Priority number: 2001 816020 ????Priority date: 22.03.2001 ????Priority country: US

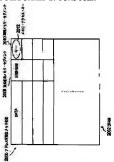
(54) COMPUTER PROCESSING SYSTEM AND PROCESSING METHOD PERFORMED IN COMPUTER

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a computer architecture and programming model for high speed processing over broadband

networks.

SOLUTION: The architecture employs a consistent modular structure, a common computing module and uniform software cells. The common computing module includes a control processor, a plurality of processing units, a plurality of local memories from which the processing units process programs, a direct memory access, controller and a shared main memory. A synchronized system and method for the coordinated reading and writing of data to and from the shared main memory by the processing units also are provided.



LEGAL STATUS

[Date of request for examination] 20.08.2002

[Date of sending the examiner's decision of rejection] [Kind of final disposal of application other than the examiner's decision of rejection or application

converted registration]

[Date of final disposal for application]

[Patent number] 3454808 [Date of registration] 25.07.2003

[Number of appeal against examiner's decision of relection]

[Date of requesting appeal against examiner's

decision of rejection]

[Date of extinction of right]